



Signal Electronics Lab

Graduate Research Assistant Opening

LAB DIRECTOR

John Hu, Ph.D.

LOCATION

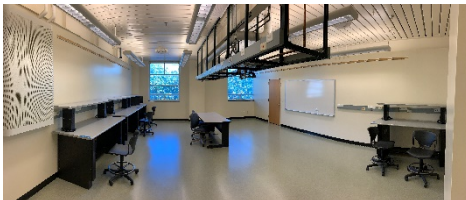
Oklahoma State University

REQUIRED QUALIFICATIONS

- Knowledge: **microelectronics, signal & systems**, and **CMOS**
- GRE > 300, TOEFL >79
- Strong academic performance
- Self-motivation for research
- Excellent communication skills

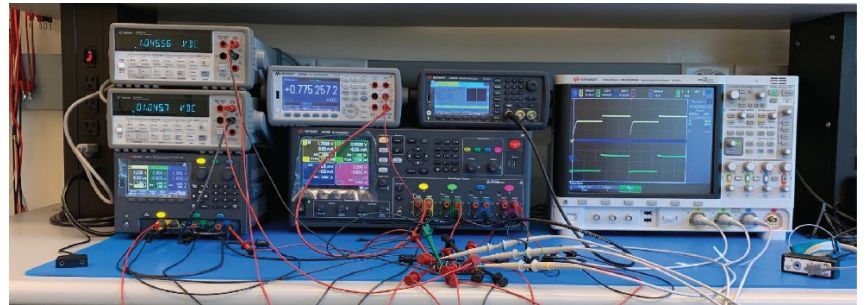
DESIRED QUALIFICATIONS

- Experience with Cadence Virtuoso (schematic, layout, PEX)
- Experience with digital IC design flow (synthesis, PnR)
- IC tape-out experience
- IC measurement experience
- Publication Record
- Knowledge in machine learning or hardware security is a plus

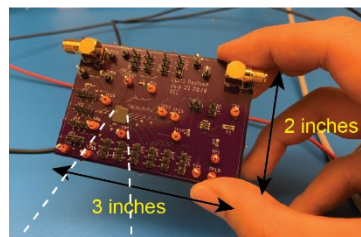


Lab space for IC assembly and test

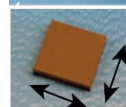
The Signal Electronics Lab at Oklahoma State University has an immediate GRA opening for a Ph.D. student to research Analog, Mixed-Signal, and Power Management VLSI design. The position is available starting in Spring or Fall 2022. The graduate research assistantship includes a monthly stipend and full tuition waiver. Successful applicants will work on CMOS integrated circuits (IC) design for energy-efficient and hardware security applications. Our alumni are highly sought after by the semiconductor industry.



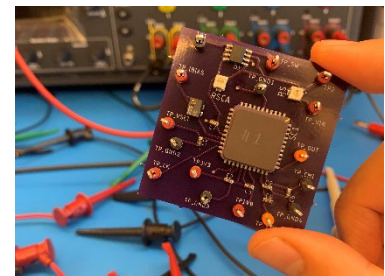
Power Management IC (LDO) Test bench on an ESD mat



PCB we designed



Chip: LD01
UMC 0.13um CMOS



Chip: SC01: TSMC 65nm CMOS

If you are interested, please send CV, transcripts, and a sample publication to john.hu@okstate.edu. The subject of the email should be "YourLastName-Application for PhD Position".