ENGINEERING SOUTH 237 | WEDNESDAY, MARCH 5 | 12-1PM
PIZZA AND DRINKS WILL BE SERVED

Vogt ECE Lunchtime Colloquium

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DR. JOHN HU, ASSISTANT PROFESSOR

John Hu is an assistant professor and Jack H. Graham Endowed Fellow of Engineering at ECE Dept of OSU. His research is the area of analog and mixed-signal VLSI design, hardware security, and cybersecurity mesh architecture.

ABSTRACT

Protecting Chips from AI Attacks: A Secure ADC Design

Did you know that chips can barely keep a secret? This talk will show you how even simple convolutional neural networks can recover all digital outputs produced by an analog-to-digital converter (ADC) circuit. With the omnipresence of ADCs in today's sensors and the growing capability and accessibility of AI, this threat underscores an urgent need for truly secure hardware in the AI era.

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