

ENGINEERING SOUTH 237 | TUESDAY, MARCH 25 | 12-1PM  
PIZZA AND DRINKS WILL BE SERVED

## Vogt ECE Lunchtime Colloquium



LUNCH & LEARN | ECE SEMINAR SERIES

### DR. SHAHRIAR SHAHABUDDIN, ASSISTANT PROFESSOR

Dr. Shahriar Shahabuddin is an Assistant Professor at the ECE Dept. of OSU and a Docent/Adjunct Professor at the Faculty of ITEE, University of Oulu, Finland. The research of the VLSI Information Processing (VIP) group, led by Dr. Sha, is at the intersection of digital VLSI architecture and signal processing.



### ABSTRACT

## Algorithm and Architecture Cooptimization for AI- Native 6G Chips:

Are you familiar with AI-Native 6G? It refers to a 6G architecture where AI is a fundamental and integral component. In this talk, we will explore the challenges and opportunities in developing digital hardware for AI-Native 6G. We will discuss how co-optimizing algorithms and architectures can be key to achieving energy-efficient chips for 6G.



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