

NSF

CHIP DESIGN RESEARCH EXPERIENCE FOR TEACHERS

The NSF Research Experience for Teachers (RET) is a year-long program for Stillwater and neighboring area high school and community college teachers (Grades 9-14). This RET includes a 6-week summer internship on semiconductors and chip design at Oklahoma State University. Participants must also translate their research experience into new curriculum modules.

INTERNSHIP BENEFITS:

- \$8,000 stipend (Does not apply to OSU employees)
- Up to \$2,000 of additional awards upon meeting program criteria
- 6-week internship in an integrated circuit (IC) design laboratory to learn about semiconductors and chip design fundamentals
- Support in developing inquiry-based pedagogy and lesson plans
- Network with fellow STEM teachers
- Opportunity to earn a free trip to Washington, DC, to present your work and their research experience into new curriculum modules.

ELIGIBILITY

- Current STEM teachers in high school or community college
- No prior knowledge of chip design is required

INTERNSHIP STRUCTURE

- Research under an OSU faculty and a graduate student mentor
- Individual and group meetings to discuss research experience
- Submit your lesson plan to TeachEngineering.org

2025

PROGRAM DATES

June 2-June 11, 2025
Hours: 9:00am - 4:00pm
Hybrid Format: In-person + Zoom

SUMMER LOCATION

Oklahoma State University
226 Engineering South,
Stillwater, OK 74078

HOW TO APPLY

Visit our Website at
ceat.okstate.edu/stem/ret
To learn more about the Chip
RET internship and apply!



APPLY HERE

QUESTIONS? CONTACT

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